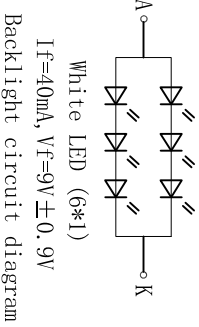


NOTE:
 WITH ' ' PLEASE CHECK
 ALL UNMARK TOLERANCE : ±0.3mm
 DIMENSION WITH * IS SIGNIFICANT DIMENSION
 DIMENSION WITH () IS REFERENCE DIMENSION



4	BACKLIGHT (With Bezel)	1
3	FPC	1
2	IC	1
1	LCD	1
NO	Part Name	Quantity

CUSTOMER APPROVAL:

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 Tel:0754-88192283 Fax:0754-86339619

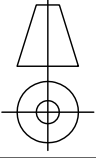
DRAWN:
 吴粤
 2020-09-14

CHECKED:
 陈秀丹 王好兴
 2020-09-14

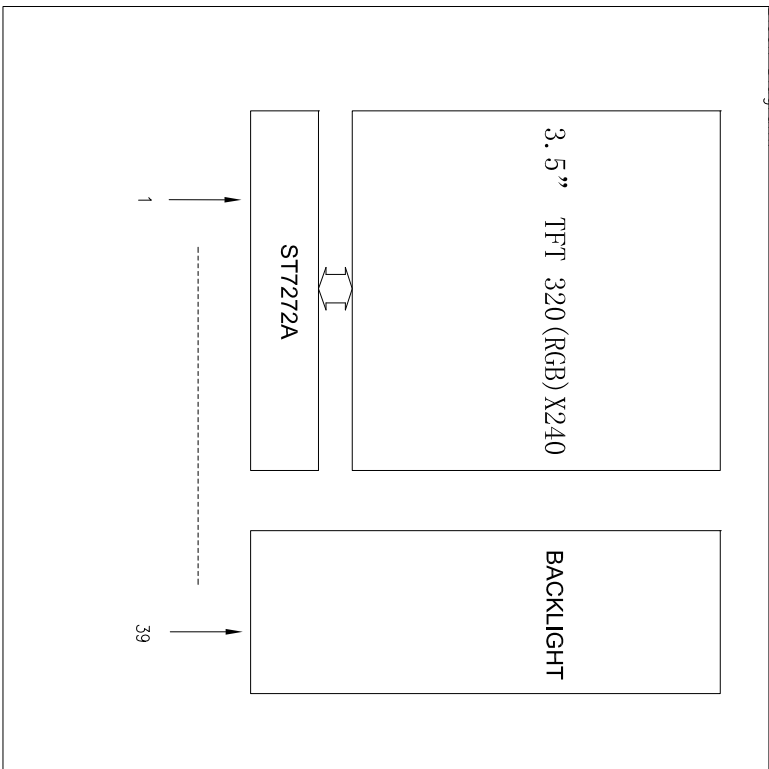
APPROVED:
 许泽宇
 2020-09-14

LCM NO: GWM/TFT17093-LCM/A
 DWG NO: ED-17093-LCM/A

Rev: 00 UNITS: mm
 SHEET 1 OF 2



Block Diagram:



DISPLAY TYPE: TFT, Normal Black, Transmissive
 VIEWING DIRECTION: Free
 DRIVER IC FOR LCD: ST7272A
 LOGIC VOLTAGE FOR LCD: IOVCC=1.8/3.3±0.1V, VDD=3.3±0.1V
 INTERFACE TYPE: 3-line SPI+ 18 bit RGB interface
 OPERATING TEMPERATURE: -20° C ~ +70° C
 STORAGE TEMPERATURE: -30° C ~ +80° C

TFM



LCD Interface Pin Description:

Pin	Symbol	Function Description
1	NC	No connection.
2	NC	No connection.
3	NC	No connection.
4	GND	LCD Power ground
5	NC	No connection.
6	A	Power for LED backlight anode
7	K	Power for LED backlight cathode
8	GND	LCD Power ground
9	IOVCC	Power supply for I/O system.
10	VDD	Power supply for analog and booster circuits
11	TE	Teating effect signal output pin
12	CS	Chip selection signal, active low
13	SCL	SPI Interface clock signal
14	SDA	SPI Interface input signal
15	SDO	SPI Interface output signal
16	B0	
17	B1	
18	B2	6-bit data bus display blue data
19	B3	
20	B4	
21	B5	
22	G0	
23	G1	
24	G2	6-bit data bus display green data
25	G3	
26	G4	
27	G5	
28	R0	
29	R1	
30	R2	6-bit data bus display red data
31	R3	
32	R4	
33	R5	
34	DE	Data enable signal
35	DCLK	Data clock
36	HSYNC	Horizontal sync signal
37	VSYNC	Vertical sync signal
38	RESET	Reset signal, active low
39	GND	LCD Power ground

A00: 首版, 对应DM17096的TFM

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许泽宇

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LCM NO: GWMTF17093-LCM/A

Rev: 00

UNITS: mm

DWG NO: ED-17093-LCM/A

SHEET

2 OF 2

