

WHITE LED (*4)
 1 led=20mA
 Vled=10.8V (MIN) ~12.0V (TYP) ~13.2V (MAX)
 BACKLIGHT CIRCUIT
 Module Luminance Intensity(LCM) : 450 cd/m² Typ.

5	BEZ-UP	1
4	Backlight (with BEZ-DOWN)	1
3	PPC	1
2	IC	1
1	TFT	1
NO	Part Name	Quantity

General tolerance $\pm 0.3\text{mm}$
 Marked with () is reference dimension.

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 2018-01-07

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 2018-01-07

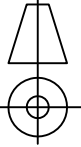
APPROVED: 江树仁
 2018-01-07

LCM NO: GWM/TFT15955-LCM/A
 DWG NO: ED-15955-LCM/A

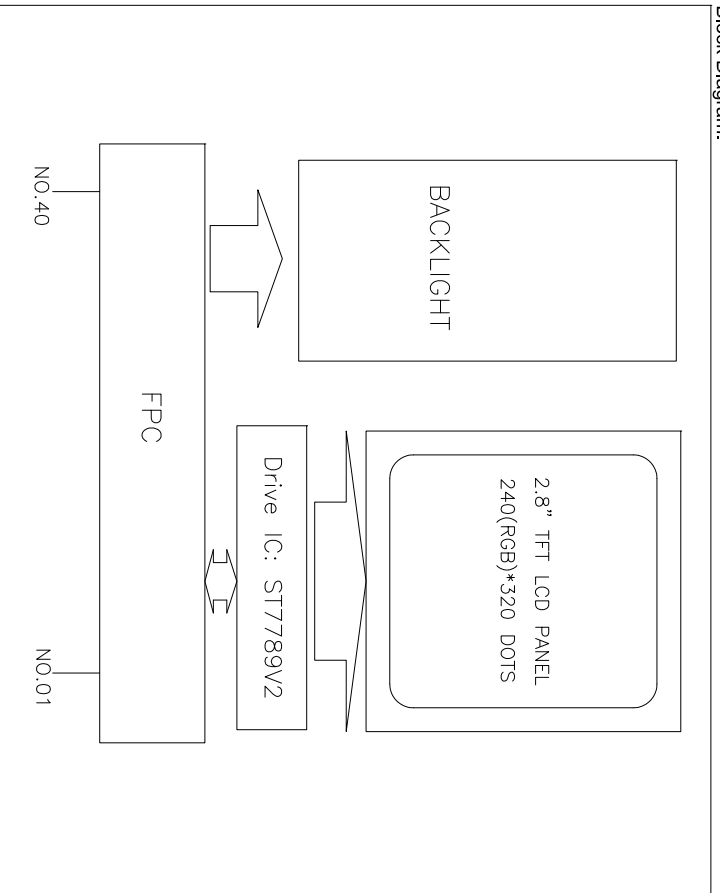
Rev: 00 UNITS: mm
 SHEET 1 OF 2

CUSTOMER APPROVAL:

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Block Diagram:



LCM Pin Description:

PIN	Symbol	Function Description	
1	NC	No connect	
2	A	LED anode	
3	K	LED cathode	
4	NC	No connect	
5	GND	Ground	
6	VDD	Power supply	
7	TE	Output a frame head pulse signal	
8	CSX	Chip select signal input pin (low active)	
9	WE(X)S	Write signal input pin in parallel interface	
10	DO(X)SCL	Data/command select pin in parallel interface	
11	RDX	Read signal input pin	
12	SDA	SPI Interface input/output pin	
13	SDO	SPI Interface output pin	
14	DB17	18-bit parallel bi-directional data bus	
15	DB16		
16	DB15		
17	DB14		
18	DB13		
19	DB12		
20	DB11		
21	DB10		
22	DB9		
23	DB8		
24	DB7	18-bit parallel bi-directional data bus	
25	DB6		
26	DB5		
27	DB4		
28	DB3		
29	DB2		
30	DB1		
31	DB0		
32	EMABLE		Data enable signal for RGB interface
33	DOTCLK		Dot clock signal for RGB interface
34	HSYNG	Line synchronizing signal for RGB interface	
35	VSYNG	Frame synchronizing signal for RGB interface	
36	RESX	Reset signal input pin (low active)	
37	IM3	The MCU interface mode select	
38	IM2		
39	IM1		
40	IM0		

DISPLAY TYPE: TFT/Normal White (65K/262K Color)
 DISPLAY MODE: Transmissive
 VIEWING DIRECTION: 3:00(for best image quality)
 LCD DRIVER IC: ST7789V2
 LCD POWER VOLTAGE: IOVCC=VDD=2.8~3.3V
 OPERATING TEMPRATURE: -20° ~ +70° C
 STORAGE TEMPRATURE: -30° ~ +80° C
 INTERFACE CONNECTOR: FPC
 ALL UNMARKED TOLERANCE: ± 0.3mm
 () REFERENCE DIMENSION



Revision History:

Rev	Rev	Rev	Rev	MPU Interface Mode
0	0	0	0	80-8bit parallel I/F
0	0	0	1	80-16bit parallel I/F
0	0	1	0	80-9bit parallel I/F
0	0	1	0	80-18bit parallel I/F
0	1	0	1	3-line 9bit serial I/F
0	1	0	1	2 data lane serial I/F
0	1	1	0	4-line 8bit serial I/F
1	0	0	0	80-16bit parallel I/F II
1	0	0	1	80-8bit parallel I/F II
1	0	1	0	80-18bit parallel I/F II
1	0	1	1	80-9bit parallel I/F II
1	1	0	1	3-line 9bit serial I/F II
1	1	1	0	4-line 8bit serial I/F II

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