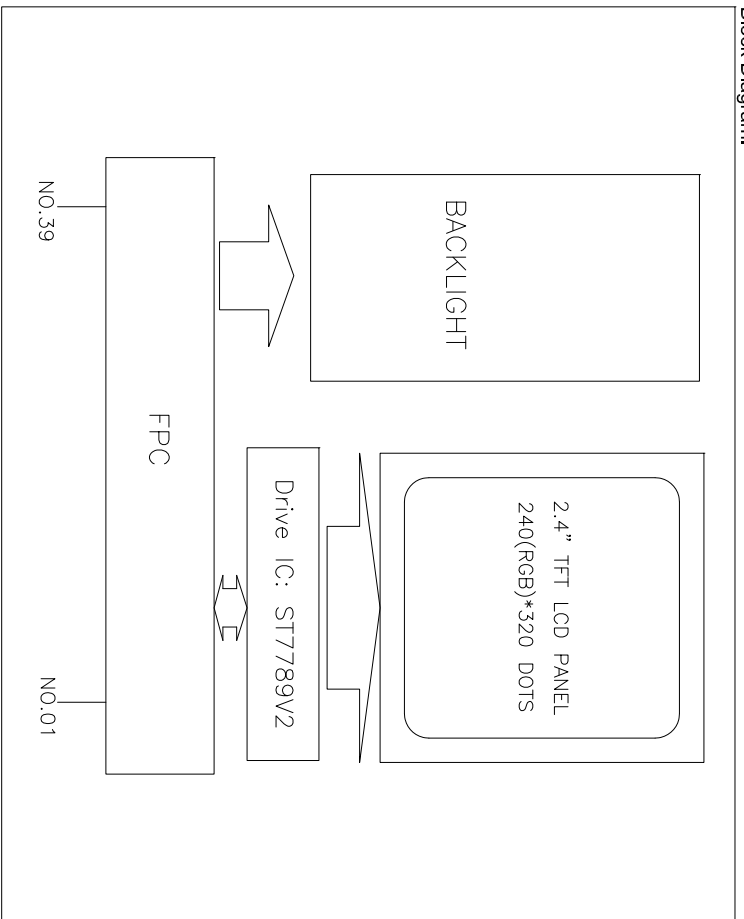


Block Diagram:



DISPLAY TYPE: TFT/Normal White (65K/262K Color)
 DISPLAY MODE: Transmissive
 VIEWING DIRECTION: 6:00
 LCD DRIVER IC: ST7789V2
 LCD POWER VOLTAGE: $10V_{CC}=V_{DD}=2.8V \sim 3.3V$
 OPERATING TEMPERATURE: $-20^{\circ} \sim +70^{\circ} C$
 STORAGE TEMPERATURE: $-30^{\circ} \sim +80^{\circ} C$
 INTERFACE CONNECTOR: FPC

LCD Pin Description:

PIN	Symbol	Function Description	
1	A	LED anode	
2	K	LED cathode	
3	NC	No connect	
4	GND	Ground	
5	VDD	Power supply	
6	TE	Output a frame head pulse signal	
7	CSX	Chip select signal input pin (low active)	
8	WR(XRS)	Write signal input pin in parallel interface	
9	DR(XSCL)	Data/command select pin in parallel interface	
10	RDX	Read signal input pin	
11	SDA	SPI interface input/output pin	
12	SDO	SPI interface output pin	
13	DB17	18-bit parallel bi-directional data bus	
14	DB16		
15	DB15		
16	DB14		
17	DB13		
18	DB12		
19	DB11		
20	DB10		
21	DB9		
22	DB8		
23	DB7	The MCU Interface mode select	
24	DB6		
25	DB5		
26	DB4		
27	DB3		
28	DB2		
29	DB1		
30	DB0		
31	ENABLE		Data enable signal for RGB interface
32	DOTCLK		Dot clock signal for RGB interface
33	HSYNC	Line synchronizing signal for RGB interface	
34	VSXNC	Frame synchronizing signal for RGB interface	
35	RESX	Reset signal input pin (low active)	
36	IM3		
37	IM2		
38	IM1		
39	IM0		

IM3	IM2	IM1	IM0	MCU Interface Mode
0	0	0	0	80-16bit parallel I/F
0	0	0	1	80-16bit parallel I/F
0	0	1	0	80-40bit parallel I/F
0	0	1	1	80-18bit parallel I/F
0	1	0	0	3-line 9bit serial I/F
0	1	0	1	2 data lane serial I/F
0	1	1	0	4-line 8bit serial I/F
0	1	1	1	80-16bit parallel I/F-II
1	0	0	0	80-8bit parallel I/F-II
1	0	0	1	80-18bit parallel I/F-II
1	0	1	0	80-40bit parallel I/F-II
1	0	1	1	3-line 9bit serial I/F-II
1	1	0	0	4-line 8bit serial I/F-II
1	1	0	1	



A00: Original Version
 Revision History:

CUSTOMER APPROVAL:

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DRAWN:

谢肯定
 2018-12-29

CHECKED:

江树仁
 2018-12-29

APPROVED:

江树仁
 2018-12-29

LCM NO: GWMTP15952-LCM/A
 DWG NO: ED-15952-LCM/A

Rev: 00 UNITS: mm
 SHEET 2 OF 2

